

CTRQ 2019

The Twelfth International Conference on Communication Theory, Reliability, and Quality of Service

ISBN: 978-1-61208-699-6

March 24 - 28, 2019

Valencia, Spain

CTRQ 2019 Editors

Jaime Lloret Mauri, Polytechnic University of Valencia, Spain

CTRQ 2019

Forward

The Twelfth International Conference on Communication Theory, Reliability, and Quality of Service (CTRQ 2019), held between March 24, 2019 and March 28, 2019 in Valencia, Spain, continued a series of events focusing on the achievements on communication theory with respect to reliability and quality of service. The conference also brought onto the stage the most recent results in theory and practice on improving network and system reliability, as well as new mechanisms related to quality of service tuned to user profiles.

We take here the opportunity to warmly thank all the members of the CTRQ 2019 technical program committee, as well as all the reviewers. The creation of such a high quality conference program would not have been possible without their involvement. We also kindly thank all the authors who dedicated much of their time and effort to contribute to CTRQ 2019. We truly believe that, thanks to all these efforts, the final conference program consisted of top quality contributions.

We also thank the members of the CTRQ 2019 organizing committee for their help in handling the logistics and for their work that made this professional meeting a success.

We hope that CTRQ 2019 was a successful international forum for the exchange of ideas and results between academia and industry and to promote further progress in the domain of communication theory, reliability, and quality of service. We also hope that Valencia, Spain provided a pleasant environment during the conference and everyone saved some time to enjoy the historic charm of the city.

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Data Loss in RAID-5 Storage Systems with Latent Errors

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Abstract—Storage systems employ redundancy and recovering schemes to protect against device failures and latent sector errors, and to enhance reliability. The effectiveness of these schemes has been evaluated based on the Mean Time to Data Loss (MTTDL) and the Expected Annual Fraction of Data Loss (EAFDL) metrics. The reliability degradation due to device failures has been assessed in terms of both these metrics, but the adverse effect of latent errors has been assessed only in terms of the MTTDL metric. This article addresses the issue of evaluating the amount of data losses caused by latent errors. It presents a methodology for obtaining MTTDL and EAFDL of RAID-5 systems analytically in the presence of unrecoverable or latent errors. A theoretical model capturing the effect of independent latent errors and device failures is developed, and closed-form expressions are derived for the metrics of interest.

Keywords-Storage; Unrecoverable or latent sector errors; Reliability analysis; MTTDL; EAFDL; RAID; MDS codes; stochastic modeling.

I. INTRODUCTION

Today's large-scale data storage systems use data redundancy schemes to recover data lost due to device and component failures, and to enhance reliability [1]. Erasure coding schemes are deployed that provide high data reliability as well as high storage efficiency. Special cases of erasure codes are the replication schemes and the Redundant Arrays of Inexpensive Disks (RAID) schemes, such as RAID-5 and RAID-6, that have been deployed extensively in the past thirty years [2-5]. The effectiveness of these schemes has been evaluated based on the Mean Time to Data Loss (MTTDL) [2-11] and, more recently, the Expected Annual Fraction of Data Loss (EAFDL) reliability metrics [1][12][13]. The introduction of the latter metric was motivated by the fact that Amazon S3 considers the durability of data over a given year [14], and, similarly, Facebook [15], LinkedIn [16] and Yahoo! [17] consider the amount of data lost in given periods.

The reliability of storage systems is also degraded by the occurrence of unrecoverable or latent sector errors, that is, of errors that cannot be corrected by the standard sector-associated error-correcting code (ECC) nor by the re-read mechanism of hard-disk drives (HDDs). The effect of latent errors is quite pronounced in higher-capacity HDDs and storage nodes because of the high frequency of these errors [18-22]. The risk of irrecoverable loss of data rises in the presence of latent errors.

Analytical reliability expressions for MTTDL that take into account the effect of latent errors have been obtained predominately using Markovian models, which assume that component failure and rebuild times are independent and exponentially distributed [8][20][21][23]. The effect of latent errors on MTTDL of erasure coded storage systems for the practical case of non-exponential failure and rebuild time distributions was assessed in [22].

In this article, we consider the effect of latent errors not only MTTDL, but also on the amount of data lost for the case of non-exponential failure and rebuild time distributions. Clearly, when a data loss occurs, the amount of data lost due to a device failure is much larger than the amount of sectors lost due to latent errors. We present a non-Markovian methodology for deriving the MTTDL and EAFDL metrics analytically for the case of RAID-5 systems. We extend the methodology developed in prior work [12][13] to assess MTTDL and EAFDL of storage systems in the absence of latent errors. The validity of this methodology for accurately assessing the reliability of storage systems was confirmed by simulations in several contexts [4][9][12][24]. It was demonstrated that theoretical predictions for the reliability of systems comprised of highly reliable storage devices are in good agreement with simulation results. Consequently, the emphasis of the present work is on theoretically assessing the effect of latent errors on system reliability. This is the first work to study the effect of latent errors on EAFDL.

The remainder of the article is organized as follows. Section II describes the storage system model and the corresponding parameters considered. Section III considers the unrecoverable or latent errors and the frequency of their occurrence. Section IV presents the general framework and methodology for deriving the MTTDL and EAFDL metrics analytically for the case of RAID-5 systems and in the presence of latent errors. Closed-form expressions for relevant reliability metrics, such as the probability of data loss and the amount of data loss, are derived. Section V presents numerical results demonstrating the effectiveness of the RAID-5 scheme for improving system reliability and the adverse effect of unrecoverable or latent errors on the probability of data loss and on the MTTDL and EAFDL reliability metrics. Section VI provides a discussion concerning the results obtained. Finally, we conclude in Section VII.

II. STORAGE SYSTEM MODEL

The storage system considered here comprises n storage devices (nodes or disks), with each device storing an amount c of data, such that the total storage capacity of the system is n c. User data is divided into blocks (or symbols) of a fixed size s (e.g., sector size of 512 bytes) and complemented with parity symbols to form codewords.

TABLE I. NOTATION OF SYSTEM PARAMETERS

Parameter	Definition
n	number of storage devices
c	amount of data stored on each device
1	number of user-data symbols per codeword $(l \ge 1)$
m	total number of symbols per codeword $(m > l)$
(m,l)	MDS-code structure
8	symbol size
N	number of devices in a RAID-5 array $(N = m)$
b	average reserved rebuild bandwidth per device
R	time required to read (or write) an amount c of data at an average
	rate b from (or to) a device
$F_R(.)$	cumulative distribution function of R
$F_{\lambda}(.)$	cumulative distribution function of device lifetimes
$se^{(RAID-5)}$	storage efficiency of redundancy scheme ($se^{(\text{RAID-5})} = l/m$)
	amount of user data stored in the system $(U = se^{(\text{RAID-5})} n c)$
	number of codewords stored in a RAID-5 array $(C = c/s)$
μ^{-1}	mean time to read (or write) an amount c of data at an average rate
	b from (or to) a device $(\mu^{-1} = E(R) = c/b)$
λ^{-1}	mean time to failure of a storage device
	$(\lambda^{-1} = \int_0^\infty [1 - F_\lambda(t)] dt)$

A. Redundancy

We consider an (m, l) = (N, N - 1) maximum distance separable (MDS) erasure code, which is a mapping from N-1user-data symbols to a set of N symbols, called a codeword, having the property that any subset containing N-1 of the N symbols of the codeword can be used to decode (reconstruct, recover) the codeword. A single parity symbol is computed by using the XOR operation on l = N - 1 user-data symbols to form a codeword with m = N symbols in total. Such a scheme can tolerate a single erasure anywhere in the codeword. The N symbols of each codeword are stored on N distinct devices. More specifically, this scheme is used by the popular RAID-5 system, in which the n devices are arranged in groups (or arrays), each with N devices, one of which is redundant [2][3]. The storage system therefore comprises n/N RAID-5 arrays with each array having the ability to tolerate one device failure. The storage efficiency $se^{(\text{RAID-5})}$ of the system is given by

$$se^{(\text{RAID-5})} = \frac{l}{m} = \frac{N-1}{N}$$
 (1)

Consequently, the amount of user data U stored in the system is given by

$$U = se^{(\text{RAID-5})} nc = \frac{lnc}{m}.$$
 (2)

Also, the number C of codewords in a device is given by

$$C = \frac{c}{s} . \tag{3}$$

Our notation is summarized in Table I. The parameters are divided according to whether they are independent or derived, and are listed in the upper and lower part of the table, respectively.

B. Codeword Reconstruction

When a storage device of an array fails, the C codewords stored in the array lose one of their symbols. Subsequently, the system starts to reconstruct the lost codeword symbols using the surviving symbols of the affected codewords. We assume that device failures are detected instantaneously, which immediately triggers the rebuild process. A certain proportion of the device bandwidth is reserved for data recovery during the rebuild process, where b denotes the actual average reserved rebuild bandwidth per device. This bandwidth is usually only



Figure 1. Rebuild for a RAID-5 array with N = m = 8 and l = 7.

a fraction of the total bandwidth available at each device, the remaining bandwidth being used to serve user requests.

The rebuild process attempts to restore the codewords of the affected array sequentially. The lost symbols are reconstructed directly in a spare device as shown in Figure 1. Decoding and re-encoding of data are assumed to be done on the fly, so the reconstruction time is equal to the time taken to read and write the required data to the spare device. Consequently, the time required to recover the amount c of data lost is equal to the time R required to read (or write) an amount c of data from (or to) a device. In particular, $1/\mu$ denotes the average time required to read (or write) an amount c of data from (or to) a device, which is given by

$$\frac{1}{\mu} \triangleq E(R) = \frac{c}{b} . \tag{4}$$

C. Failure and Rebuild Time Distributions

We adopt the model and notation considered in [13]. The lifetimes of the *n* devices are assumed to be independent and identically distributed, with a cumulative distribution function $F_{\lambda}(.)$ and a mean of $1/\lambda$. We consider real-world distributions, such as Weibull and gamma, as well as exponential distributions that belong to the large class defined in [24]. The storage devices are characterized to be *highly reliable* in that the ratio of the mean time $1/\mu$ to read all contents of a device (which typically is on the order of tens of hours), to the mean time to failure of a device $1/\lambda$ (which is typically on the order of thousands of hours) is very small, that is,

$$\frac{\lambda}{\mu} = \frac{\lambda c}{b} \ll 1 .$$
 (5)

We consider storage devices whose cumulative distribution function F_{λ} satisfies the condition

$$\mu \int_0^\infty F_\lambda(t) [1 - F_R(t)] dt \ll 1, \quad \text{with } \frac{\lambda}{\mu} \ll 1 , \qquad (6)$$

where $F_R(.)$ is the cumulative distribution function of the rebuild time R. Then the MTTDL and EAFDL reliability metrics tend to be insensitive to the device failure distribution, that is, they depend only on its mean $1/\lambda$, but not on its density $F_{\lambda}(.)$ [13].

III. DATA LOSS FROM UNRECOVERABLE ERRORS

The reliability of RAID-5 systems is affected by the occurrence of unrecoverable or latent errors. Let P_{bit} denote the unrecoverable bit-error probability. According to the specifications, P_{bit} is equal to 1×10^{-15} for SCSI drives and

 1×10^{-14} for SATA drives [8]. Assuming that bit errors occur independently over successive bits, the unrecoverable sector (symbol) error probability P_s is given by

$$P_s = 1 - (1 - P_{\rm bit})^s , \qquad (7)$$

with s expressed in bits. Assuming a sector size of 512 bytes, the equivalent unrecoverable sector error probability is $P_s \approx P_{\rm bit} \times 4096$, which is 4.096×10^{-12} in the case of SCSI and 4.096×10^{-11} in the case of SATA drives. However, empirical field results suggest that the actual values can be orders of magnitude higher reaching $P_s \approx 5 \times 10^{-9}$ [25].

IV. DERIVATION OF MTTDL AND EAFDL

The MTTDL metric assesses the expected amount of time until some data can no longer be recovered and therefore is irrecoverably lost whereas the EAFDL assesses the fraction of stored data that is expected to be irrecoverably lost by the system annually. We briefly review the general methodology for deriving the MTTDL and EAFDL metrics presented in [12]. This methodology does not involve Markovian analysis and holds for general failure time distributions, which can be exponential or non-exponential, such as the Weibull and gamma distributions that satisfy condition (6).

At any point in time, the system can be thought to be in one of two modes: normal mode or rebuild mode. During normal mode, all devices are operational and all data in the system has the original amount of redundancy. Any symbols encountered with unrecoverable or latent errors are corrected through the RAID-5 capability. However, multiple unrecoverable errors encountered in a codeword can no longer be recovered and therefore lead to data loss. A transition from normal mode to rebuild mode occurs when a device fails; we refer to the device failure that causes this transition as a *first-device* failure. During rebuild mode, an active rebuild process attempts to restore the lost data in a spare device, which eventually leads the system either to an irrecoverable data loss (DL) with probability P_{DL} or back to the original normal mode by restoring initial redundancy, which occurs with probability $1 - P_{\rm DL}$.

Let T be a typical interval of a fully operational period, that is, the time interval from the time t that the system is brought to its original state until a subsequent first-device failure occurs. For a system comprising n devices with a mean time to failure of a device equal to $1/\lambda$, the expected duration of T is given by [12]

$$E(T) = 1/(n\lambda) , \qquad (8)$$

and MTTDL by

$$\text{MTTDL} \approx \frac{E(T)}{P_{\text{DL}}} = \frac{1}{n \,\lambda \, P_{\text{DL}}} \;. \tag{9}$$

The EAFDL is obtained as the ratio of the expected amount of user data lost, normalized to the amount of user data, to the expected duration of T [12, Equation (9)]:

$$\text{EAFDL} \approx \frac{E(Q)}{E(T) \cdot U} \stackrel{(8)}{=} \frac{n \,\lambda \, E(Q)}{U} \stackrel{(2)}{=} \frac{m \,\lambda \, E(Q)}{l \, c} , \quad (10)$$

with E(T) and $1/\lambda$ expressed in years.

The expected amount E(H) of data lost, given that a data loss has occurred, is given by [12, Equation (8)]:

$$E(H) = \frac{E(Q)}{P_{\rm DL}} . \tag{11}$$

From (9) and (10), it follows that the derivation of the MTTDL and EAFDL metrics requires the evaluation of P_{DL} and E(Q), respectively. These quantities are derived using the direct path approximation [4][24][26], which, under conditions (5) and (6), accurately assesses the reliability metrics of interest [11][12][24][27].

A. Rebuild Process

When a storage device of an array fails, the C codewords stored in the array lose one of their symbols. Using the directpath-approximation methodology, we proceed by considering only the subsequent potential data losses and device failures related to the affected array.

1) Unrecoverable Failure: The rebuild process attempts to restore the C codewords of the affected array sequentially. Let us consider such a codeword and let L be the number of symbols irrecoverably lost and I be the number of symbols encountered with unrecoverable errors in the codeword. As P_s is the probability that a symbol has a latent (unrecoverable) error, $1 - P_s$ is the probability that a symbol can be read successfully and, owing to the independence of symbol errors, it therefore holds that

$$P(I=i) = \binom{m-1}{i} P_s^i (1-P_s)^{m-1-i}, \text{ for } i = 0, \dots, m-1,$$
(12)

such that

$$E(I) = \sum_{i=1}^{m-1} i P(I=i) = (m-1) P_s .$$
 (13)

Clearly, the symbol lost due to the device failure can be corrected through the RAID-5 capability only if the remaining m-1 symbols can be read. Thus, L = 0 if and only if I = 0. Using (12), the probability q that a codeword can be restored is given by

$$q = P(L = 0) = P(I = 0) = (1 - P_s)^{m-1}$$
. (14)

Note that if a codeword cannot be restored, then at least one of its l user-data symbols is lost. We now deduce that the probability P_{UF} of encountering an unrecoverable failure (UF) during the rebuild process of the C codewords is given by

$$P_{\rm UF} = 1 - q^C \stackrel{(14)}{=} 1 - (1 - P_s)^{(m-1)C} .$$
 (15)

Furthermore, such an unrecoverable failure entails the loss of user data. Let us denote by $N_{\rm UF}$ the number of codewords that cannot be recovered owing to unrecoverable failures. Then it holds that

$$E(N_{\rm UF}) = C(1-q)$$
. (16)

Remark 1: For very small values of P_s , it holds that $(1 - P_s)^{(m-1)C} \approx 1 - (m-1)CP_s$. Consequently, it follows from (15) that

$$P_{\rm UF} \approx \begin{cases} (m-1) \, C \, P_s \,, & \text{for } P_s \ll P_s^{(2)} \\ 1 \,, & \text{for } P_s \gg P_s^{(2)} \,. \end{cases}$$
(17)

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where $P_s^{(2)}$ is obtained from the approximation (17) as follows:

$$P_{\rm UF} \approx (m-1) \, C P_s^{(2)} = 1 \; \Rightarrow \; P_s^{(2)} \triangleq \frac{1}{C} \cdot \frac{1}{m-1} \; . \; \; (18)$$

Note also that from (14) and (16), it follows that

$$E(N_{\rm UF}) \approx C(m-1) P_s$$
, for $P_s \ll \frac{1}{m-1}$. (19)

In particular, for $P_s = P_s^{(2)}$, it holds that $E(N_{\rm UF}) \approx 1$ and this, combined with the fact that $P_{\rm UF} \approx 1$, implies that almost surely one of the C codewords cannot be recovered owing to an unrecoverable failure.

If I > 0, the number L of symbols lost is equal to I + 1. Consequently, the expected number E(L) of symbols lost is given by

$$E(L) = \sum_{i=1}^{m-1} (i+1) P(I=i) = E(I) + 1 - P(I=0) , \quad (20)$$

and using (12), (13), and (14) yields

$$E(L) = 1 - q + (m - 1)P_s = 1 - (1 - P_s)^{m-1} + (m - 1)P_s.$$
(21)

Remark 2: For small values of P_s , it holds that $q = (1 - P_s)^{m-1} \approx 1 - (m-1) P_s$. Consequently, it follows from (21) that

$$E(L) \approx 2(m-1)P_s$$
, for $P_s \ll \frac{1}{m-1}$. (22)

In particular, the expected number E(L|L > 0) of symbols lost, given that the codeword cannot be restored, is given by

$$E(L|L>0) = \frac{E(L)}{P(L>0)} = \frac{E(L)}{1 - P(L=0)} \stackrel{(14)}{=} \frac{E(L)}{1 - q}$$

$$\stackrel{(22)}{\approx} 2, \quad \text{for } P_s \ll \frac{1}{m - 1}.$$
(23)

2) Device Failure: A subsequent device failure (DF) may occur during the rebuild process triggered by the initial device failure. The probability $P_{\text{DF}|\text{R}}$ that one of the m-1 remaining devices in the array fails during the rebuild process depends on the duration of the corresponding rebuild time R and the aggregate failure rate of these m-1 highly reliable devices, and is given by [24]

$$P_{\rm DF|R} \approx (m-1)\,\lambda\,R\,\,. \tag{24}$$

In particular, it was shown in [28, Lemma 2] that, for highly reliable devices satisfying conditions (5) and (6), the fraction of the rebuild time R still remaining when another device fails is approximately uniformly distributed between 0 and 1. This implies that the probability $P_{\text{DF}}(j|R)$ that a device failure occurs while reconstructing the jth $(1 \le j \le C)$ codeword during the rebuild process, and given a rebuild time of R, is equal to $P_{\text{DF}|R}/C$, which, using (24), yields

$$P_{\rm DF}(j|R) \approx \frac{(m-1)\,\lambda\,R}{C} , \quad \text{for } j = 1, 2, \dots, C .$$
 (25)

The probability P_{DF} of a device failure during the rebuild process is obtained by unconditioning (24) on R, that is,

$$P_{\rm DF} = E(P_{\rm DF|R}) \approx (m-1) \,\lambda \, E(R) \stackrel{(4)}{=} (m-1) \,\frac{\lambda}{\mu} \,. \tag{26}$$

Similarly, the probability $P_{\text{DF}}(j)$ of a subsequent device failure during the reconstruction of the *j*th $(1 \le j \le C)$ codeword is obtained by unconditioning (25) on *R*, that is,

$$P_{\rm DF}(j) = E(P_{\rm DF}(j|R)) \approx \frac{(m-1)\lambda E(R)}{C} \stackrel{(4)}{=} \frac{(m-1)}{C} \frac{\lambda}{\mu}.$$
(27)

B. Data Loss

Data loss may occur because of another device failure or an unrecoverable failure of one or more codewords, or a combination thereof. Note that in all cases, data loss cannot involve only parity data, but also loss of user data. Let $P_{\rm DL}$ denote the probability of data loss. Then, the probability $1 - P_{\rm DL}$ of the rebuild being completed successfully is equal to the product of $1 - P_{\rm DF}$, the probability of not encountering a device failure during a rebuild, and $1 - P_{\rm UF}$, the probability of not encountering an unrecoverable failure during the rebuild, namely, $1 - P_{\rm DL} = (1 - P_{\rm DF})(1 - P_{\rm UF})$. Consequently,

$$P_{\rm DL} = P_{\rm DF} + (1 - P_{\rm DF}) P_{\rm UF} .$$
(28)

Substituting (15) and (26) into (28) yields

$$P_{\rm DL} \approx (m-1) \,\frac{\lambda}{\mu} + \left[1 - (m-1) \,\frac{\lambda}{\mu}\right] \,\left[1 - (1 - P_s)^{(m-1) \, C}\right].$$
(29)

Remark 3: It follows from (17) and (26) that the region $[0, P_s^{(1)}]$ of P_s in which the probability $P_{\rm UF}$ is much smaller than the probability $P_{\rm DF}$ of encountering a device failure during the rebuild process is obtained by

$$P_{\rm UF} \ll P_{\rm DF} \quad \Leftrightarrow \quad (m-1) C P_s \ll (m-1) \frac{\lambda}{\mu}$$
$$\Leftrightarrow \quad P_s \ll P_s^{(1)} \triangleq \frac{1}{C} \cdot \frac{\lambda}{\mu} . \tag{30}$$

C. Amount of Data Loss

Depending on whether a subsequent device failure occurs during the rebuild process, two cases are considered:

1) No Device Failure during Rebuild: The probability of this event is equal to $1 - P_{\text{DF}}$. The expected number of symbols lost due to unrecoverable errors, $E(S_{\text{U}}^{\odot} | \text{ no DF})$, is given by

$$E(S_{\rm U}^{\odot} | \text{ no DF}) = C E(L) = C [1 - q + (m - 1)P_s].$$
 (31)

Unconditioning on the event of not having a device failure during the rebuild process, and using (14) and (26), we get

$$E(S_{\mathrm{U}}^{\odot}) = E(S_{\mathrm{U}}^{\odot} | \text{ no DF})P(\text{ no DF}) = E(S_{\mathrm{U}}^{\odot} | \text{ no DF})(1 - P_{\mathrm{DF}})$$

$$\approx C \left[1 - (1 - P_s)^{m-1} + (m-1)P_s\right] \left[1 - (m-1)\frac{\lambda}{\mu}\right]$$
(32)

2) Device Failure during Rebuild: Suppose a subsequent device failure occurs while reconstructing the jth $(1 \le j \le C)$ codeword. The probability of this event, denoted by $P_{\text{DF}}(j)$, is given by (27). In this case, the two symbols of this codeword that are stored on the two failed devices can no longer be recovered and are lost. Furthermore, each of the remaining m - 2 symbols may be lost owing to unrecoverable errors with probability P_s . The same applies for the remaining C - j codewords. Thus, the total number $S_D(j)$ of symbols that are stored on the two failed devices and are lost is given by

$$S_{\rm D}(j) = 2(C+1-j)$$
. (33)

Also, the expected total number $E(S_{U}^{+} | \text{DF at } j)$ of symbols stored in these C - j + 1 codewords that are lost owing to unrecoverable errors is given by

$$E(S_{\rm U}^+ | \text{DF at j}) = (C+1-j)(m-2)P_s$$
. (34)

Furthermore, each of the j-1 codewords considered for reconstruction prior to the subsequent device failure loses an expected number of E(L) symbols. Consequently, the expected total number $E(S_{\rm U}^{-} | \text{DF at j})$ of symbols stored in these j-1 codewords that are lost owing to unrecoverable errors is given by

$$E(S_{\rm U}^- | \text{DF at j}) = (j-1) E(L)$$
 (35)

Unconditioning (33), (34), and (35) on the event of a device failure during the reconstruction of the *j*th codeword, and using (27), yields

$$E(S_{\rm D}) \approx \sum_{j=1}^{C} 2\left(C+1-j\right) \frac{(m-1)}{C} \frac{\lambda}{\mu}$$
 (36)

$$= (C+1)(m-1)\frac{\lambda}{\mu},$$
 (37)

$$E(S_{\rm U}^+) \approx \sum_{j=1}^{C} (C+1-j) \left(m-2\right) P_s \frac{(m-1)}{C} \frac{\lambda}{\mu} \qquad (38)$$

$$= \frac{C+1}{2} (m-1) (m-2) P_s \frac{\lambda}{\mu}, \qquad (39)$$

and using (21)

$$E(S_{\rm U}^{-}) \approx \sum_{j=1}^{C} (j-1) E(L) \frac{(m-1)}{C} \frac{\lambda}{\mu}$$

$$= \frac{C-1}{2} \left[1 - (1-P_s)^{m-1} + (m-1)P_s\right] (m-1) \frac{\lambda}{\mu}.$$
(40)
(41)

Combining the two cases, and using (32), (39), and (41), the expected number $E(S_{\rm U})$ of symbols lost due to unrecoverable errors is obtained as follows:

$$E(S_{\rm U}) = E(S_{\rm U}^{\odot}) + E(S_{\rm U}^{+}) + E(S_{\rm U}^{-})$$

$$\approx C \left[1 - (1 - P_s)^{m-1} + (m-1)P_s\right]$$

$$- \frac{C+1}{2} \left[1 - (1 - P_s)^{m-1} + P_s\right](m-1)\frac{\lambda}{\mu}.$$
(42)

Remark 4: From (32), (39), and (41), it follows that $E(S_{\rm U}^{\odot}) \gg E(S_{\rm U}^{-}) > E(S_{\rm U}^{+})$ because $E(S_{\rm U}^{-})$ and $E(S_{\rm U}^{+})$ are of the order $O(\lambda/\mu)$, which is very small, whereas $E(S_{\rm U}^{\odot})$ is not. Moreover, for large C, we have $E(S_{\rm U}^{-})/E(S_{\rm U}^{+}) \approx [1-(1-P_s)^{m-1}+(m-1)P_s]/[(m-2)P_s] > 1$. In particular, for small P_s , we have $(1-P_s)^{m-1} \approx 1-(m-1)P_s$, which implies that $E(S_{\rm U}^{-})/E(S_{\rm U}^{+}) \approx 2(m-1)/(m-2) > 1$. Consequently, the symbols lost due to unrecoverable errors are predominately encountered during a rebuild that is completed without experiencing an additional device failure.

From (37) and (42), it follows that the expected total number of symbols lost E(S) is given by

$$E(S) = E(S_{\rm D}) + E(S_{\rm U})$$

$$\approx C \left[1 - (1 - P_s)^{m-1} + (m-1)P_s \right]$$

$$+ \frac{C+1}{2} \left[1 + (1 - P_s)^{m-1} - P_s \right] (m-1) \frac{\lambda}{\mu}.$$
(43)
(43)
(43)

Remark 5: For small values of P_s , it follows from (44) that

$$E(S) \approx 2C(m-1)P_s + \frac{C+1}{2}(2-mP_s)(m-1)\frac{\lambda}{\mu},$$
 (45)

which implies that for $P_s = 0$, $E(S) = E(S_D) = (C+1)(m-1)\lambda/\mu$.

Remark 6: When P_s increases and approaches 1, it follows from (44) that E(S) approaches Cm. This is intuitively obvious because when $P_s = 1$, all the Cm symbols stored in the system are lost because of unrecoverable errors.

We now proceed to derive E(Q), the expected amount of user data lost. First, we note that the expected number of user symbols lost is equal to the product of the storage efficiency to the expected number of symbols lost. Consequently, it follows from (44) that

$$E(Q) = \frac{l}{m} E(S) s \stackrel{(3)}{=} \frac{l}{m} \frac{E(S)}{C} c , \qquad (46)$$

where s denotes the symbol size. Similar expressions for the expected amounts $E(Q_{\rm D})$ and $E(Q_{\rm U})$ of user data lost due to device and unrecoverable failures are obtained from $E(S_{\rm D})$ and $E(S_{\rm U})$, respectively. Thus, from (37), (42), and (44), it follows that

$$E(Q_{\rm D}) \approx \frac{l}{m} \frac{C+1}{C} (m-1) \frac{\lambda}{\mu} c , \qquad (47)$$

$$E(Q_{\rm U}) \approx \frac{l}{m} \left\{ 1 - (1 - P_s)^{m-1} + (m-1)P_s - \frac{C+1}{2C} \left[1 - (1 - P_s)^{m-1} + P_s \right] (m-1) \frac{\lambda}{\mu} \right\} c$$
(48)

and

$$E(Q) = E(Q_{\rm D}) + E(Q_{\rm U})$$

$$\approx \frac{l}{m} \left\{ 1 - (1 - P_s)^{m-1} + (m - 1)P_s + \frac{C + 1}{2C} \left[1 + (1 - P_s)^{m-1} - P_s \right] (m - 1) \frac{\lambda}{\mu} \right\} c$$
(50)

Remark 7: For small values of P_s , and using (5), it follows from (48) that

$$E(Q_{\rm U}) \approx 2 \frac{l}{m} (m-1) c P_s .$$
 (51)

Remark 8: From (47) and (51), it follows that the region $[0, P_s^{(3)}]$ of P_s in which $E(Q_U)$ is much smaller than $E(Q_D)$

is obtained by

$$E(Q_{\rm U}) \ll E(Q_{\rm D})$$

$$\Leftrightarrow 2\frac{l}{m}(m-1)cP_s \ll \frac{l}{m}\frac{C+1}{C}(m-1)\frac{\lambda}{\mu}c$$

$$\Leftrightarrow P_s \ll P_s^{(3)} \triangleq \frac{1}{2} \cdot \frac{C+1}{C} \cdot \frac{\lambda}{\mu}.$$
(52)

Remark 9: When P_s increases and approaches 1, it follows from (50) that E(Q) approaches C l. This is intuitively obvious because when $P_s = 1$, upon the first-device failure, all the C l user-data symbols stored in the RAID-5 array are lost owing to unrecoverable errors.

D. Reliability Metrics

The MTTDL normalized to $1/\lambda$ is obtained by substituting (29) into (9) as follows:

$$\lambda MTTDL \approx$$

$$\frac{1}{n\left\{(m-1)\frac{\lambda}{\mu} + \left[1 - (m-1)\frac{\lambda}{\mu}\right]\left[1 - (1 - P_s)^{(m-1)C}\right]\right\}},$$
(53)

where C and λ/μ are given by (3) and (5), respectively.

The EAFDL normalized to λ is obtained by substituting (50) into (10) as follows:

EAFDL/
$$\lambda \approx 1 - (1 - P_s)^{m-1} + (m - 1)P_s$$

+ $\frac{C+1}{2C} [1 + (1 - P_s)^{m-1} - P_s](m - 1)\frac{\lambda}{\mu}$,
(54)

where C and λ/μ are given by (3) and (5), respectively.

The E(H) normalized to c is obtained by substituting (50) and (29) into (11) as follows:

$$\begin{split} E(H)/c &\approx \\ \frac{l}{m} \left\{ 1 - (1 - P_s)^{m-1} + (m-1)P_s \\ &+ \frac{C+1}{2C} \left[1 + (1 - P_s)^{m-1} - P_s \right] (m-1) \frac{\lambda}{\mu} \right\} \\ \left\{ (m-1) \frac{\lambda}{\mu} + \left[1 - (m-1) \frac{\lambda}{\mu} \right] \left[1 - (1 - P_s)^{(m-1)C} \right] \right\}, \end{split}$$
(55)

where C and λ/μ are given by (3) and (5), respectively.

Similarly to (11), expressions for $E(H_D)$ and $E(H_U)$, the expected amounts of user data lost due to device and unrecoverable failures, given that such failures have occurred, are obtained as follows:

$$E(H_{\rm D}) = \frac{E(Q_{\rm D})}{P_{\rm DF}}, \quad \text{and} \quad E(H_{\rm U}) = \frac{E(Q_{\rm U})}{P_{\rm UF}}, \quad (56)$$

respectively.

From (11), (49), and (56), we deduce that the following relation holds

$$E(H) = \frac{P_{\rm DF}}{P_{\rm DL}} E(H_{\rm D}) + \frac{P_{\rm UF}}{P_{\rm DL}} E(H_{\rm U}) .$$
 (57)

Note that this is not a weighted average of $E(H_D)$ and $E(H_U)$ because the events of a subsequent device failure

and of unrecoverable failures are not mutually exclusive, and therefore, and according to (28), the sum of weights is not equal to 1.

Remark 10: The normalized E(H)/c exhibits two plateaus. According to (26), (30), (47), and (52), the first plateau is in the region $[0, P_s^{(1)}]$ of P_s , that is,

$$\frac{E(H)}{c} \approx \frac{l}{m} \frac{C+1}{C} , \qquad \text{for } P_s \ll P_s^{(1)} .$$
 (58)

For the second plateau, depending on the value of λ/μ , the following two cases are considered:

Case 1: $\lambda/\mu \gg 2/[(m-1)(C+1)]$. From (18) and (52), it holds that $P_s^{(2)} \ll P_s^{(3)}$. According to (17), (18), (47), and (52), the second plateau is in the region $[P_s^{(2)}, P_s^{(3)}]$ of P_s , that is,

$$\frac{E(H)}{c} \approx \frac{l}{m} \frac{C+1}{C} (m-1) \frac{\lambda}{\mu}, \text{ for } P_s^{(2)} \ll P_s \ll P_s^{(3)}.$$
(59)

Case 2: $\lambda/\mu \ll 2/[(m-1)(C+1)]$. From (18) and (52), it holds that $P_s^{(3)} \ll P_s^{(2)}$. According to (17), (18), (51), and (52), the second plateau is in the region $[P_s^{(3)}, P_s^{(2)}]$ of P_s , that is,

$$\frac{E(H)}{c} \approx \frac{l}{m} \frac{2}{C}$$
, for $P_s^{(3)} \ll P_s \ll P_s^{(2)}$. (60)

Also, it follows from (51) that

$$\frac{E(H)}{c} \approx 2 \frac{l}{m} (m-1) P_s , \quad \text{for } P_s \gg \max(P_s^{(2)}, P_s^{(3)}) .$$
(61)

Substituting (15), (26), (47), and (48) into (56) yields

$$E(H_{\rm D})/c \approx \frac{l}{m} \frac{C+1}{C}$$
, (62)

and

$$E(H_{\rm U})/c \approx \frac{l}{m} \left\{ 1 - (1 - P_s)^{m-1} + (m-1)P_s - \frac{C+1}{2C} \left[1 - (1 - P_s)^{m-1} + P_s \right] (m-1) \frac{\lambda}{\mu} \right\} / \left[1 - (1 - P_s)^{(m-1)C} \right],$$
(63)

where C and λ/μ are given by (3) and (5), respectively.

Remark 11: For small values of P_s , substituting (17) and (51) into (56) yields

$$E(H_{\rm U})/c \approx \begin{cases} 2\frac{l}{m}\frac{1}{C}, & \text{for } P_s \ll P_s^{(2)} \\ 2\frac{l}{m}(m-1)P_s & \text{for } P_s \gg P_s^{(2)}. \end{cases}$$
(64)

Remark 12: When P_s increases and approaches 1, it follows from (55) that E(H) approaches Cl. This is intuitively obvious because when $P_s = 1$, all the Cl user-data symbols stored in the system are lost because of unrecoverable errors.

V. NUMERICAL RESULTS

We consider a RAID-5 system comprised of n = 8 devices with N = m = 8, l = 7, $\lambda/\mu = 0.001$, capacity c = 1 TB, and symbol size s equal to a sector size of 512 bytes, such that the number of codewords stored in a device is given by $C = c/s = 1.9 \times 10^9$.

The probability of data loss P_{DL} is obtained by (15), (26), and (29) as a function of the unrecoverable error probability P_s of a symbol (sector), and shown in Figure 2. It follows from (17) that, for small values of P_s , the probability $P_{\rm UF}$ of encountering an unrecoverable failure during the rebuild process increases linearly with P_s , as indicated by the dotted green line in Figure 2. According to (26), the probability P_{DF} of encountering a device failure during the rebuild process is independent of the unrecoverable symbol error probability, as indicated by the horizontal dotted blue line in Figure 2. It follows from (30) that, when P_s is in the region $[0, P_s^{(1)}]$, the probability $P_{\rm UF}$ of encountering an unrecoverable failure is much smaller than the probability $P_{\rm DF}$ of encountering a device failure during the rebuild process. From (30), and for the parameters considered, it follows that $P_s^{(1)} = 5 \times 10^{-13}$, as shown in Figure 2. Subsequently, for $P_s > P_s^{(1)}$, the probability $P_{\rm UF}$ of encountering an unrecoverable failure is much greater than that of encountering a device failure. In particular, it follows from (17) that, when $P_s \gg P_s^{(2)}$, $P_{\rm UF}$ and, in turn, P_{DL} approach 1 and are essentially independent of P_s . From (18), and for the parameters considered, it follows that $P_s^{(2)} = 7 \times 10^{-11}$, as shown in Figure 2. As expected, the total probability of data loss P_{DL} is monotonically increasing in P_s .

The normalized λ MTTDL measure is obtained by (53) and is shown in Figure 3 as a function of the unrecoverable symbol error probability. The various regions and plateaus are also depicted and correspond to the regions discussed above regarding the probability of data loss.

The normalized expected amount of user data lost to the amount of data stored in a device, E(Q)/c, is obtained by (47),



Figure 2. Probability of data loss $P_{\rm DL}$ for a RAID-5 array under latent errors $(\lambda/\mu=0.001,\,m=N=8,\,l=7,\,c=1$ TB, and s=512 B).



Figure 3. Normalized MTTDL for a RAID-5 array under latent errors ($\lambda/\mu = 0.001$, m = N = 8, l = 7, c = 1 TB, and s = 512 B).



Figure 4. Normalized amount of data loss E(Q) for a RAID-5 array under latent errors ($\lambda/\mu = 0.001$, m = N = 8, l = 7, c = 1 TB, and s = 512 B).

(48), and (50) as a function of the unrecoverable symbol error probability P_s , and shown in Figure 4. It follows from (51) that, for small values of P_s , the normalized expected amount $E(Q_U)/c$ of user data lost due to unrecoverable failures increases linearly with P_s , as indicated by the dotted green line in Figure 4. According to (47), the normalized expected amount $E(Q_D)/c$ of user data lost due to a subsequent device failure during the rebuild process is independent of the unrecoverable symbol error probability, as indicated by the horizontal dotted blue line in Figure 4. As anticipated, the total expected amount E(Q) of user data lost increases monotonically with P_s . In particular, when P_s approaches 1 and according to Remark 9, the normalized expected amount E(Q)/c of user data lost approaches l = 7, as all user data is lost.

The normalized EAFDL/ λ measure is obtained by (54) and is shown in Figure 5 as a function of the unrecoverable symbol



Figure 5. Normalized EAFDL for a RAID-5 array under latent errors ($\lambda/\mu = 0.001$, m = N = 8, l = 7, c = 1 TB, and s = 512 B).

error probability. Equation (10) suggests that this measure is proportional to E(Q), which implies that the preceding discussion regarding the behavior of E(Q) also holds here. Note also that, although the fraction of data loss never exceeds 1, EAFDL can exceed 1 because it expresses the annual fraction of data loss, which also takes into account the frequency of data losses.

The normalized expected amount E(H)/c of user data lost, given that a data loss has occurred, to the amount of data stored in a device is obtained by (55), (62), and (63) as a function of the unrecoverable symbol error probability P_s , and shown in Figure 6. In contrast to the $P_{\rm DL}$, EAFDL, and E(Q) measures that increase monotonically with P_s , we observe that E(H)does not.

Data losses occur because of a subsequent device failure, unrecoverable failures of codewords, or a combination thereof. According to (62), the expected amount $E(H_D)$ of user data lost associated with a subsequent device failure, given that such a device failure has occurred during the rebuild process, is independent of the unrecoverable symbol error probability, as indicated by the horizontal dotted blue line in Figure 6. Such a device failure causes the loss of many symbols as opposed to a small number of additional symbols that may be lost owing to unrecoverable failures. In particular, according to Remark 2 and (23), each of the codewords that cannot be restored loses approximately two symbols. When P_s is extremely small, an unrecoverable failure is very unlikely, but when this occurs, it is caused by encountering a single codeword that cannot be recovered, which in turn results in the loss of two symbols. Consequently, and according to (64), for P_s such that $P_s \ll P_s^{(2)} = 7 \times 10^{-11}$, the expected amount $E(H_{\rm U})$ of user data lost due to unrecoverable failures, given that such unrecoverable failures have occurred, is independent of P_s , as indicated by the horizontal part of the dotted green line shown in Figure 6. Also, the amount of data lost corresponding to the two symbols lost is negligible compared with the amount of data lost due to a subsequent device failure, that is, $E(H_{\rm U}) \ll E(H_{\rm D})$.



Figure 6. Normalized E(H) for a RAID-5 array under latent errors ($\lambda/\mu = 0.001$, m = N = 8, l = 7, c = 1 TB, and s = 512 B).

The combined expected amount E(H) of user data lost, given that a data loss has occurred, is an average of $E(H_D)$ and $E(H_{\rm U})$ with weights expressed by (57). For $P_s \ll P_s^{(1)} =$ 5×10^{-13} , a data loss is most likely attributed to a device failure, which results in the first plateau expressed by (58). However, for values of P_s in the region $[5 \times 10^{-13}, 7 \times 10^{-11}]$, this is reversed, in that it becomes more likely to encounter an unrecoverable failure than a device failure, and this causes $P_{\rm DL}$ to increase as shown in Figure 2. Consequently, as the weight of the $E(H_D)$ component decreases, so does E(H). Subsequently, as P_s increases further, this weight can no longer decrease because P_{DI} has reached its maximum value of 1. Also, according to (19) and (64), the number of codewords with unrecoverable failures and the corresponding amount of data lost $E(H_{\rm U})$ increase linearly in P_s , but, although $E(H_{\rm U})$ increases, as indicated by the dotted green line in Figure 6, it still remains negligible compared with $E(H_D)$. Consequently, E(H) no longer decreases and stabilizes at the second plateau level given by (59). As P_s increases further and exceeds $P_s^{(3)} = 5 \times 10^{-4}$, the increasing amount of data lost due to unrecoverable failures $E(H_{\rm U})$ exceeds $E(H_{\rm D})$, which in turn leads to an increase of the E(H) metric. In particular, when P_s approaches 1, and according to Remark 9, the amount lc of user data stored in the RAID-5 array is lost owing to unrecoverable errors, which in turn implies that the normalized expected amount E(H)/c of user data lost approaches l = 7.

VI. DISCUSSION

As discussed in Section III, field results suggest that the probability of unrecoverable sector errors lies in the range $[4.096 \times 10^{-11}, 5 \times 10^{-9}]$. Figure 3 shows that MTTDL is significantly degraded by the presence of latent errors, whereas Figure 5 reveals that EAFDL is practically unaffected in this range. When the probability of unrecoverable sector errors lies in the region of practical interest, the probability of encountering an unrecoverable failure is much larger than that of encountering a device failure, which degrades MTTDL. However, the amount of sectors lost due to latent errors is negligible compared with the amount of data lost due to a

device failure, which in turn implies that EAFDL remains unaffected. In contrast, Figure 6 reveals that the expected amount E(H) of data lost, given that a data loss has occurred, decreases in the region of practical interest. This is due to the fact that when a data loss occurs, it is more likely caused by a unrecoverable failures that involve the loss of a small number of sectors rather than by a device failure that results in a significantly larger amount of data lost.

It follows from (30) and (52) that

$$P_s^{(1)} = \frac{1}{C} \cdot \frac{\lambda}{\mu} \ll \frac{1}{2} \cdot \frac{C+1}{C} \cdot \frac{\lambda}{\mu} = P_s^{(3)} .$$
 (65)

Consequently, increasing P_s first affects P_{DL} , MTTDL, and E(H) and then E(Q) and EAFDL.

VII. CONCLUSIONS

The effect of latent sector errors on the reliability of RAID-5 data storage systems was investigated. A methodology was developed for deriving the Mean Time to Data Loss (MTTDL) and the Expected Annual Fraction of Data Loss (EAFDL) reliability metrics analytically. Closed-form expressions capturing the effect of unrecoverable latent errors were obtained. We established that the reliability of storage systems is adversely affected by the presence of latent errors. The results demonstrated that the effect of latent errors depends on the relative magnitudes of the probability of encountering a latent error versus the probability of encountering a device failure. It was found that, for actual values of the unrecoverable sector error probability, MTTDL is adversely affected by the presence of latent errors, whereas EAFDL is not.

Extending the methodology developed to derive the MTTDL and EAFDL reliability metrics of erasure coded systems in the presence of unrecoverable latent errors is a subject of further investigation.

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Comprehensive Security Framework of an Intelligent Wastewater Purification System for Irrigation

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Abstract— Although Internet of Things (IoT) is a growing and evolving technology, attackers are exploiting the multiple weaknesses of IoT devices. Some attackers even employ these devices to perform massive attacks, which can greatly affect the Internet. Furthermore, the data centers for the obtained data from sensing IoT devices are comprised of several devices: hardware, software and the communication equipment. Therefore, the data center requires a secure and controlled environment. It is then evident the necessity of implanting end-to-end integral security in IoT environments. In this paper, we propose an integral security method for our previously published intelligent wastewater purification system for irrigation. The security is performed from the physical device to the data stored at the data center including its transport utilizing different technologies and going through various pieces of equipment.

Keywords- IoT; Sensors; Actuators; Security; LoRaWAN; Attacks.

I. INTRODUCTION

The Internet of Things plays an extraordinary role in all aspects of life. Moreover, billions of "things" are communicating to each other: from TVs, fridges and cars to smart meters, health monitors, agriculture monitors and wearables. Therefore, communication through wireless networks, cloud computing and data communication among IoTs are increasing [1]. This opens-up exciting new opportunities for research in academia and industry. However, it also opens the door to a variety of new security threats.

The weaknesses of the IoT devices allow accessing the systems where they are being employed. There is a rapid

growth of botnets according to the report in [2]. Botnets are a net of bots or robots that can perform autonomously and automatically and can be jointly controlled. If attackers employ these bots, it may lead to major attacks to the Internet. Furthermore, the report indicates that many attacks to the corporative network are unleashed after compromising vulnerable servers and computing resources.

Wastewater treatment systems for irrigation benefit from using different IoT technologies for monitoring and managing information of the irrigation process. A smart irrigation system relies on sensors and online services for improved efficiency. Users can control the system from a remote device and can configure it using a cloud service.

These systems face different types of attacks which can be physical attacks, network attacks and system attacks. For physical attacks, the problem includes destroying or stealing the components of the IoT devices. For communication attacks, the lack of security and vulnerability of the channel for data communication allow the attacker to sniff information from the network. In the system, unsecure data storage allows the attacker to obtain information from the cloud environment. Therefore, unauthorized people may modify and steal information from the system.

There are some existing researches that proposed security measures against the attacks to IoT systems. A design of IoT based on smart security and monitoring for farming is proposed in [3]. Therefore, IoT systems based on smart security and monitoring devices for agriculture are being developed. In the proposal described in [4], real time notifications are provided based on information analysis and processing used for the identification of rodents and threats. Furthermore, IoT challenges and opportunities are presented in [5], where tasks such as trusted sensing, computation, communication, privacy, and digital forgetting are addressed.

In this paper, we analyze and mitigate the security threats faced by a smart irrigation system by designing security architectures that safeguard the IoT devices and data and ensure the correct performance of the IoT system. The main measures can be described as follows:

• Preparing a protective box to protect from the physical damage of IoT components such as sensors, accessing the serial port, and the battery when people or animals tamper with them.

• Providing secure data communication for both wire and wireless connection modes by establishing a VPN (Virtual Private Network) in order to avoid eavesdropping.

• Utilizing a security mechanism to alert and determine the attempt of attacks to the Fog Computing system. Therefore, data encryption and Fog security provides a comprehensive portfolio for cloud service and enterprises.

• Using access management encryption techniques to provide data security when unauthorized people request and want to modify the data.

• Securing the servers at the data center where the data obtained from the sensor nodes is stored.

• Securing the network devices employed to forward the data to the data centers.

The rest of the paper is organized as follows. The related work is presented in Section II. Section III depicts the system description. In Section IV, we present how the system should be secured. Lastly, the conclusion and future work is presented in Section V.

II. RELATED WORK

The security of IoT is important because the world is becoming very sensitive and there is constant fear about threats, thieves and other dangerous situations. Therefore, launching malicious attacks against irrigation systems can have a significant impact on water utilities and their customers. Authors of [6] proposed a smart water management model combining IoT technologies with business processes coordination and decision support systems. They defined the management exploitation layer, coordination layer, subsystems layer, administration layer and the interfaces that enable layer interaction. Their proposed architecture can be used for controlling real water management systems and dealing with many real problems such as physical network definition or mapping identifiers.

The authors of [7] analyzed the security requirements specific to IoT systems by taking into consideration network security, identity management, privacy, trust, and resilience. They presented mechanisms that ensure data confidentiality, integrity, origin authentication and freshness for each IoT technology. A large selection of IoT technologies was analyzed, from single-layer protocols to full protocol stacks.

The security level of LoRaWan (LPWAN, Low Power Wide Area Network) is studied in [8]. It is focused on the security of LoRaWan. The LoRaWan protocol is a MAC (Medium Access Control) layer protocol for LoRa, which provides the communication infrastructure and interfaces for gateway-sensor topologies, node coordination, and medium access. They proposed several countermeasures and changes to the LoRaWan protocol, which rendered all these attack vectors harmless. Many of these countermeasures can be implemented with minimal changes to the LoRa ecosystem.

The authors in [9] proposed a lightweight algorithm, which is based on a set of rules to detect the characteristics of the packets in the network. The proposed approach can detect the malicious packets that are sent to the network. Furthermore, the authors of [10] reviewed the architecture and features of fog computing, including real-time services and fog-assisted IoT applications based on the different roles of fog nodes. They presented security and privacy threats towards IoT applications.

Other authors have studied partial solutions to punctual problems related to the security of the system, but they do not provide an integral solution. The principal contribution of our proposal is an integral security solution for the complete system. It includes structured proposals based on four differentiated categories based on where the attacks are intended for in order to achieve security for all physical devices as well as the flow of forwarded data and stored data. On our proposed solution, we address a wide variety of devices, network equipment and data that integrate different technologies. The use of multiple technologies leads to an increase of the complexity of the solution.

III. SYSTEM DESCRIPTION

In this section, we will describe the location of the devices and the different technologies used by our intelligent wastewater purification system for irrigation.

As it can be seen in Fig. 1, our system is distributed in different locations. In some of them, we have implemented groups formed by nodes that can contain sensors or actuators. Red circles with black dots inside identify groups of sensor nodes. The black dots of each circle represent a sensor node. Stars identify actuators nodes. The data is transmitted wirelessly using LoRaWan between all the nodes and among the node groups. Besides, there is a remote location, which will be reached through wired technology, where our data storage center and the equipment to carry out the work with Artificial Intelligence (AI) are located.

Both the sensor nodes and the actuator nodes are distributed in external locations. For this reason, they will be protected by integrating them in watertight boxes to withstand water, weather variations and possible animal attacks.

From one of the group of nodes, that is located in an urban area, a WAN connection is established with the remote location through an Internet Service Provider (ISP). In the remote location, the network devices that are connected to the ISP and to the different available equipment are deployed. The Data Center and the equipment that utilizes AI to process the information collected by the sensors is deployed at the remote location as well. Behavior patterns will be detected through AI and decision rules will be applied in the actuator nodes according to the detected patterns.

The vulnerabilities of the systems to control water, which is performed through actuator nodes in our water



Figure 1. Location of devices and equipment.

management proposal, may compromise the objective of our system. Severe problems of both irrigation saturation and shortage as well as an unwanted lack of control of the water purification system may be generated. Furthermore, attackers may obtain the control of the nodes so as to perform massive attacks, which may affect the Internet.

IV. SECURING THE SYSTEM

For our system, it is paramount to guarantee the availability and accessibility to all the devices at any time.

At the report in [2], an attack to a water and wastewater treatment plant managed by an international company, the attackers compromised the internal network by launching an attack from a server located on the demilitarized zone (DMZ). It was discovered that the attackers performed the following steps:

• The DMZ server had been violated due to a bad policy that allowed establishing RDP connections.

• The server was violated and controlled through different IPs (Internet Protocol) by different attackers that were enemies to the company.

Those attackers performed more attacks to other plants of the same company.

All systems that manage information or provide a service are susceptible of being attacked. Irrigation and water management systems may not manage information that must remain private but ensuring the correct performance of the system is crucial. Furthermore, these systems are not only susceptible of attacks from people with malicious intentions but also may be affected by animals, weather conditions and people that accidentally compromise the system. The scenario where people is interested in controlling the waterflows of the canals according to their own interests is also possible.

The main problem with IoT devices is that they are not designed considering that their security is paramount on certain fields. Once the network security is stablished, it is important to perform a penetration test on the different components that comprise the system so as to verify the effectiveness of the solution.



Figure 2. Basic scheme of security and data flow.

In the next sections, we describe and propose the solutions for our system. Fig. 2 shows the basic security scheme that we will implement on our system. The three network areas that we are going to utilize can be observed. They are the IoT network area, the ISP network area, and the network area of the data center and the AI equipment.

As it can be seen, two firewalls will protect the areas of the network that belong to us from the shared network. The green arrows represent the data flows that will be established between the IoT network, the data center and the AI. Considering the way the system performs its functions and its vulnerabilities, attacks can be classified into four categories: physical attacks, attacks on the data in transit, attacks on the management system and attacks on the data.

A. Physical attacks

Physical attacks comprise those attacks that require the physical access to the device. In the case of our proposed system, removing the sensors, accessing the nodes through the serial port or removing the batteries are some of the physical attacks that can be performed.

Another important thing to consider in our system is the possible presence of animals that could bite the wires or take the devices. Thus, nodes should be enclosed on a protective box so as to protect the device from possible attackers and the elements. The box should include a combination lock and the password to the lock should only be known by the administrator of the system. Moreover, considering that the nodes are deployed over several irrigation canals, there is a possibility of losing the devices in case of flooding or extreme rain. This security issue cannot be avoided, but the system includes the mechanisms so as to alert of malfunctioning or lost nodes. Therefore, the administrator would know to replace the damaged devices in case of these types of events.

It is also important to consider that most of the nodes, as well as network devices and the computers employed on our system, are provided with different access ports. For all the equipment, we will only utilize the ports necessary to transmit the information or to access the administration of the different devices and equipment. We will disable the rest of the ports so as to avoid the unauthorized access from attackers external to the project.

Another important remark is reducing the usage of USB (Universal Serial Bus) memory drives or other external memory devices. These elements are connected to the computing resources and may be infected with malware. By utilizing just the necessary ports and previously analyzing them, it is possible to avoid subsequent attacks. Furthermore, physical security is also very important at the transmission

network and at the different storage and computation devices.

B. Attacks on the data in transit

These attacks would aim at making the system unable to communicate or intercepting the transmitted data so as to gain knowledge on the information gathered by the system.

It is necessary for our system to be protected against eavesdropping and tampering. In order to achieve it, the combination of forwarded data encryption and the protection of the networks where the transmission is performed should be performed. Attackers may try accessing through the physical network infrastructure or through the software components provided by the services of the network itself. The impact of an attack performed utilizing software components would probably be greater [11].

Our proposed system utilizes both wired and wireless communication, and both types of communication should be secured. The most effective way of establishing greater security over the wired network is having a proprietary telecommunications service comprised of an optic fiber network. However, this proposal is not viable. For this reason, for the wired communication, a VPN will be established so as to avoid eavesdropping and access to the network.

As for LoRaWan, replay attacks, jamming techniques and wormhole attacks could compromise the communications [8]. Replay attacks consist on fooling the device by utilizing old information that has been retransmitted. However, the attacker should know the channels and the employed frequencies so as to sniff the transmission. This attack is addressed by keeping track of frame counters as seen in Fig. 3. Jamming consists on disrupting radio transmissions by transmitting high power radio signals in the vicinities of the nodes. The spreading factor and transmissions on the same frequency may cause interferences among the devices. Although this attack is difficult to avoid, it is easier to detect, and proper actions can be taken so as to re-establish communication. If the nodes of the network begin to lose the connection, the communication frequency should be changed. Lastly, wormhole attacks consist on capturing packets and replaying to the source so as to avoid them reaching the gateway. Regretfully, this type of attacks is hard to detect but utilizing previous analyzed data, abnormal behaviors could be detected.

Authentication between end-device and the network is one of the implemented security measures. Moreover, LoRaWan also incorporates end-to-end encryption [12]. It utilizes AES (Advanced Encryption Standard) with CMAC (Cipher-based Message Authentication Code) and CTR (Counter) to provide integrity and encryption respectively. The authentication is performed with a unique identifier and 128-bit AES key. AES-CMAC is utilized to compute the Message Integrity Code (MIC). Furthermore, LoRaWan

1 Byte	4 Bytes	1 Byte	2 Bytes	0-15 Bytes		4 Bytes
MAC Header	Node Address			Frame Options	Payload	

128 bit AES key encrypted

Figure 3. Frame counters.

already addresses some security issues by implementing several security measures.

C. Attacks on the management system

Our proposed system utilizes Fog computing so as to manage alerts and control the nodes from the edge. However, an attack on the Fog server could compromise the correct functioning of the system. Fog servers are susceptible of attacks of Denial of Service (DoS), Man in the Middle (MITM) or placing a rogue gateway [13].

The structure of Fog computing implies that DoS attacks would affect the closest networks but not the whole system if several Fog servers are utilized. Furthermore, the utilized protocols and their security mechanisms have to be considered and will become determinant in the success or failure of the attack. Man in the middle attacks could intercept data such as the alerts of the system, resulting in the system not taking the corresponding actions to resolve the problem if these messages are not forwarded. Tracking the messages and detecting abnormal behavior could help in detecting these types of attacks.

If this attack is utilized for eavesdropping, proper encryption techniques should be utilized. Furthermore, the system should be designed so as to avoid the introduction of rogue gateways that could disrupt the correct functioning of the system.

Moreover, all devices must be configured with passwords and access codes that will only be known by the authorized management personal. As usual, the personal entrusted with the management and administration of the system will employ different passwords to access the devices and their personal accounts. Furthermore, different access levels to management privileges will be established according to the activities related to each job position.

If our devices are not provided with a strong authentication system, some of the previously addressed problems may arise. These attacks may be denegation of service, modification and/or data thief, brute force attacks, etc. Device monitoring, freezing accounts and throttling are some of the common measures that we will employ so as to avoid the fraudulent access to the devices. Actualizing firmware and the operating systems of all the utilized devices is critical. All devices and equipment must update throughout their lifespan. Furthermore, the number of equipment with external access to the Internet browser must be reduced.

D. Attacks on the data

The data gathered from the system could be compromised by a third party. Unauthorized people could acquire the data or modify it so as to disrupt the correct performance of the system. The aspects that concern data security are integrity, confidentiality and availability [14].

In the case of our proposed system, the integrity and availability of the data is preferred to its confidentiality. However, enough encryption techniques should be utilized so as to provide the system with enough confidentiality. Furthermore, the information stored on Fog servers and the

ABLE I. PROPOSALS TO REJECTS ATTACKS

Attacks	How our proposal refuses the attack
Access to physical	Hardening devices and facilities.
device.	Access control in the facilities.
	Alarms.
Compromised physical	Ligar/pageword agoog
Device.	Visual identity verification (authentication phase).
Compromised node.	Use of algorithms to detect compromised nodes. Change of trust level. trust elimination.
Malfunctioning or lost	Replacement of the damaged devices.
the nodes or equipment.	
Power failure.	Equipment protection against failure to supply
	power. Persistent storage.
Lost data because of	Persistent storage.
failures or battery discharge.	Authentication.
Access to user date in	User/password access.
physical device.	Privileges management.
Malware.	other external memory devices Control ports.
Loose of connectivity.	Persistent data storage. Authentication
Identity impersonation.	Visual identity verification (authentication phase).
	Control ports.
	Use of short-range technologies.
	Firewall. Trust policies
	Thus policies.
Phishing, active	Hashing and authentication.
spoofing, compromised	Use of a trusted chain.
data	VPN IPSec. Firewall
Network data access	Control ports
using passive spoofing.	Ciphered using session key.
	Key management.
	Firewall.
Access to network key	Asymmetric encryption.
(man-in-the-middle)	Firewall.
()	
Access to private user	Asymmetric or symmetric encryption guaranteeing
delivered data using	confidentiality.
passive spoofing	VPN IPSec.
Compromised data	VPN IPSec.
Overload and/or loose	Capacity plan and forecast.
of resources.	Control the number of asymmetric operations.
	Firewall.
Emonocus no altota	Persistent data storage.
delivery	Packet retransmission.
Data storage overload	Distributed data management and storage.
Denial-of-Service /	Distributed data management and storage.
Data avalia0111ty	security processes.
	Firewall.
Access to not reliable	Data access only through trusted nodes.
data.	Session key regeneration.
Data disclosure.	

cloud should be properly protected. Secure passwords to access the data, authentication, encrypted data forwarding,

and guaranteeing that user accounts cannot be duplicated are some of the measures to be considered.

At the data center, our data is previously protected by a firewall that prevents the connection of unauthorized users from the shared network (ISP). Moreover, the access permissions and updates of operating systems and firewall software of the different equipment must be adequate. These tasks must be performed on all implemented proprietary network resources as well.

The data of our system must be isolated from the data from any other network at all time. This way, a direct transfer can be avoided. Table 1 summarizes the proposals to reject the attacks.

Fig. 4 shows the summary of the phases of our security system. At the start, the proposals defined in Table 1 should be applied to the different equipment and devices.

V. CONCLUSION AND FUTURE WORK

We have performed a study on the different key points that may affect the security of our intelligent wastewater purification system for irrigation. We define four possible attack categories as being physical attacks, attacks on the communications, attacks on the management system, and attacks on the data. Furthermore, we specify the security measures that we will apply for each category for our system to be considered as secure.

As future work, we will implement the proposed policies in a real environment, we will perform penetration tests so as to verify its viability and we will correct any problem that we may detect. Furthermore, we will consider the implementation of an Ad-hoc network for our system utilizing secure protocols as the one in [15].



ACKNOWLEDGMENT

This work has been partially supported by the "Ministerio de Economía y Competitividad" in the "Programa Estatal de Fomento de la Investigación Científica y Técnica de Excelencia, Subprograma Estatal de Generación de Conocimiento" within the project under Grant TIN2017-84802-C2-1-P. This work has also been partially supported by European Union through the ERANETMED (Euromediterranean Cooperation through ERANET joint activities and beyond) project ERANETMED3-227 SMARTWATIR.

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